

FIG. 1

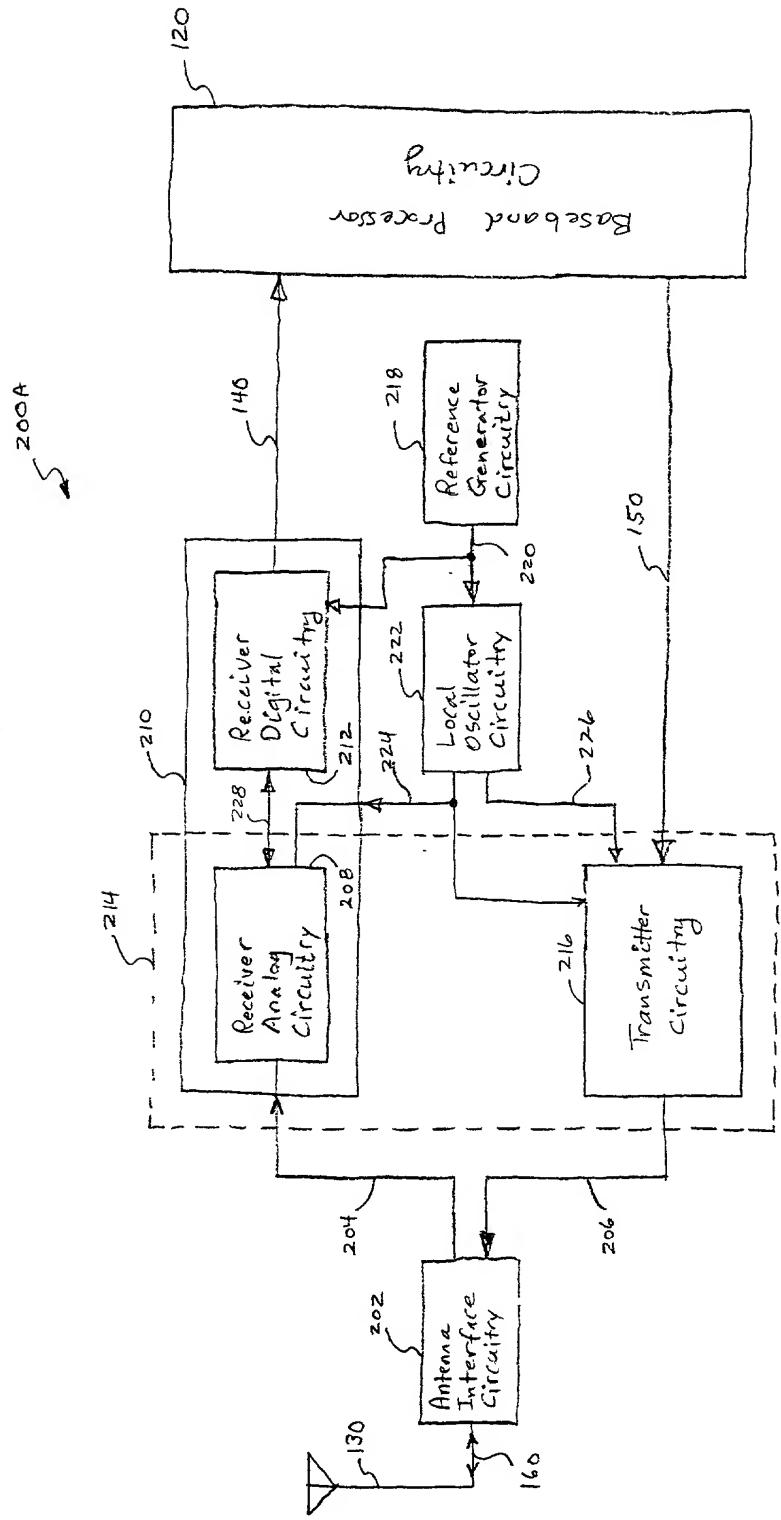


FIG. 2A

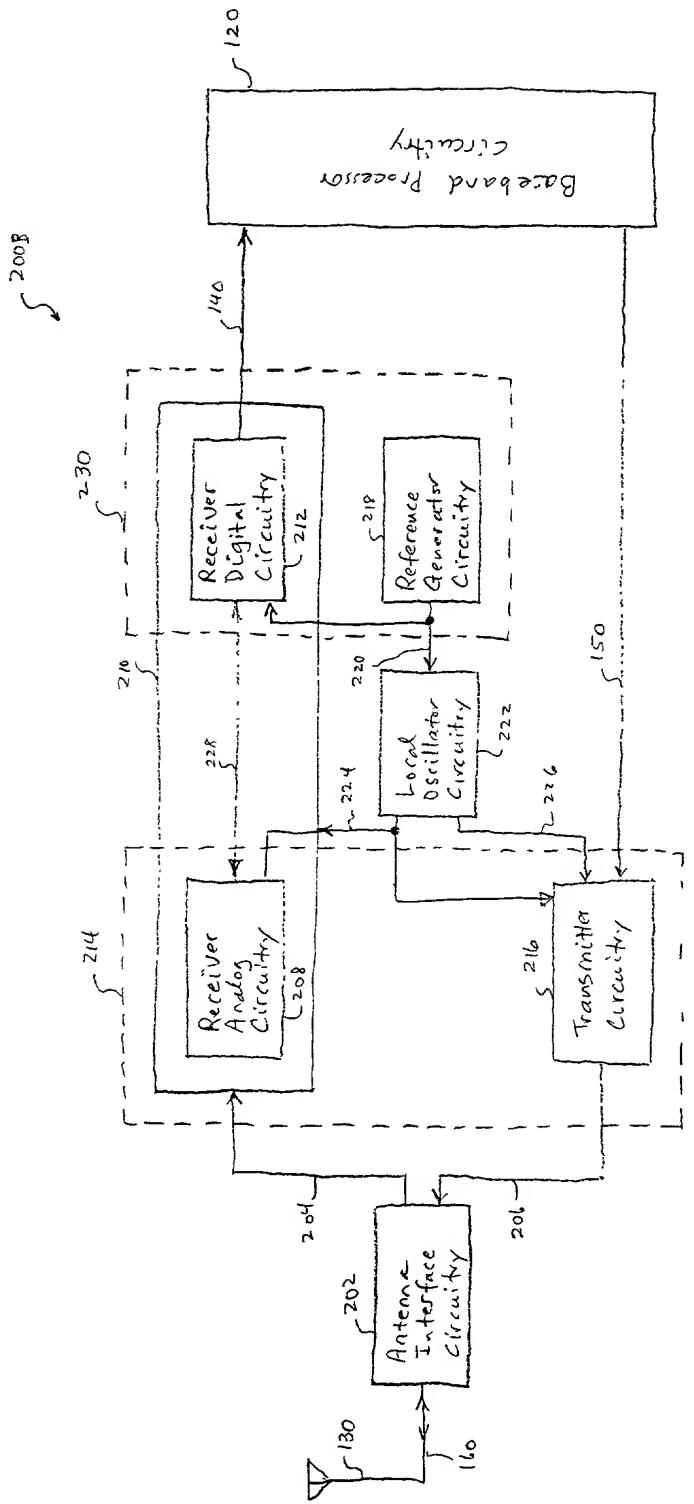


FIG. 7B

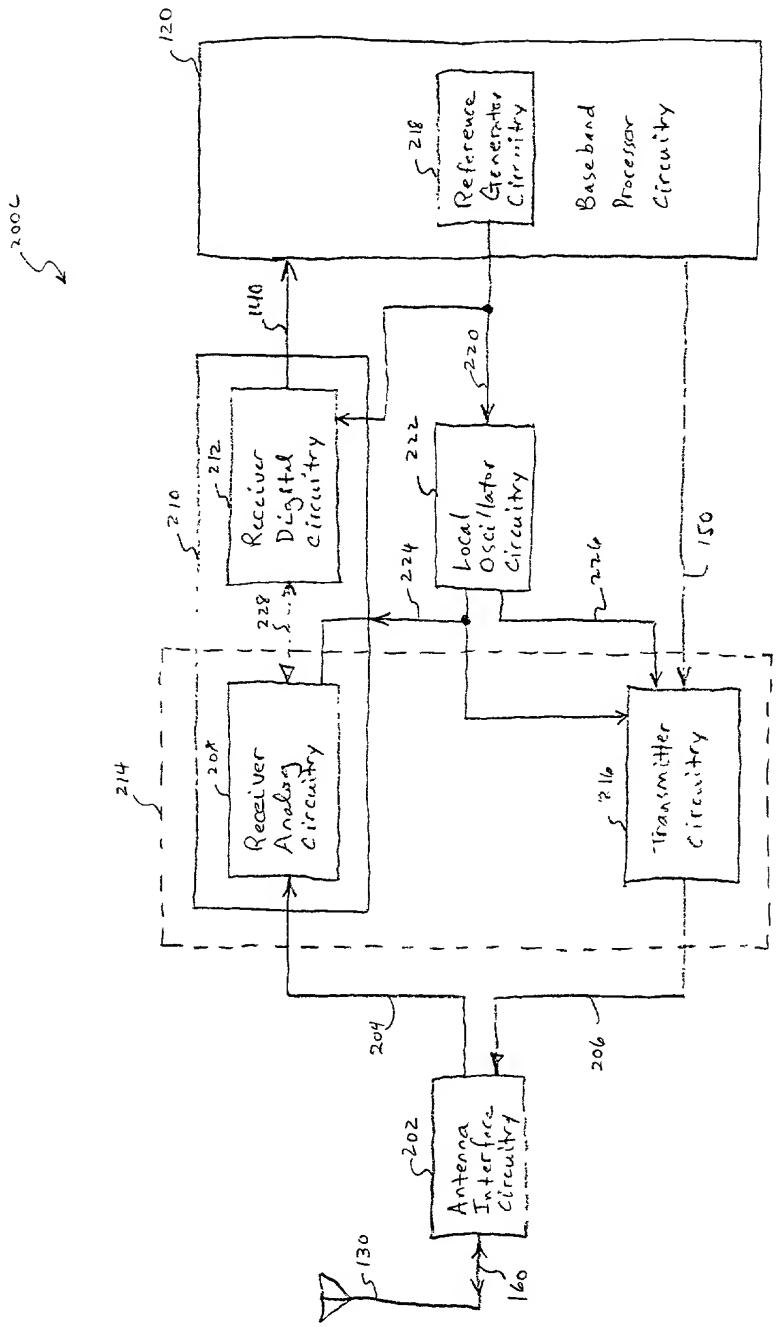


FIG. 2C

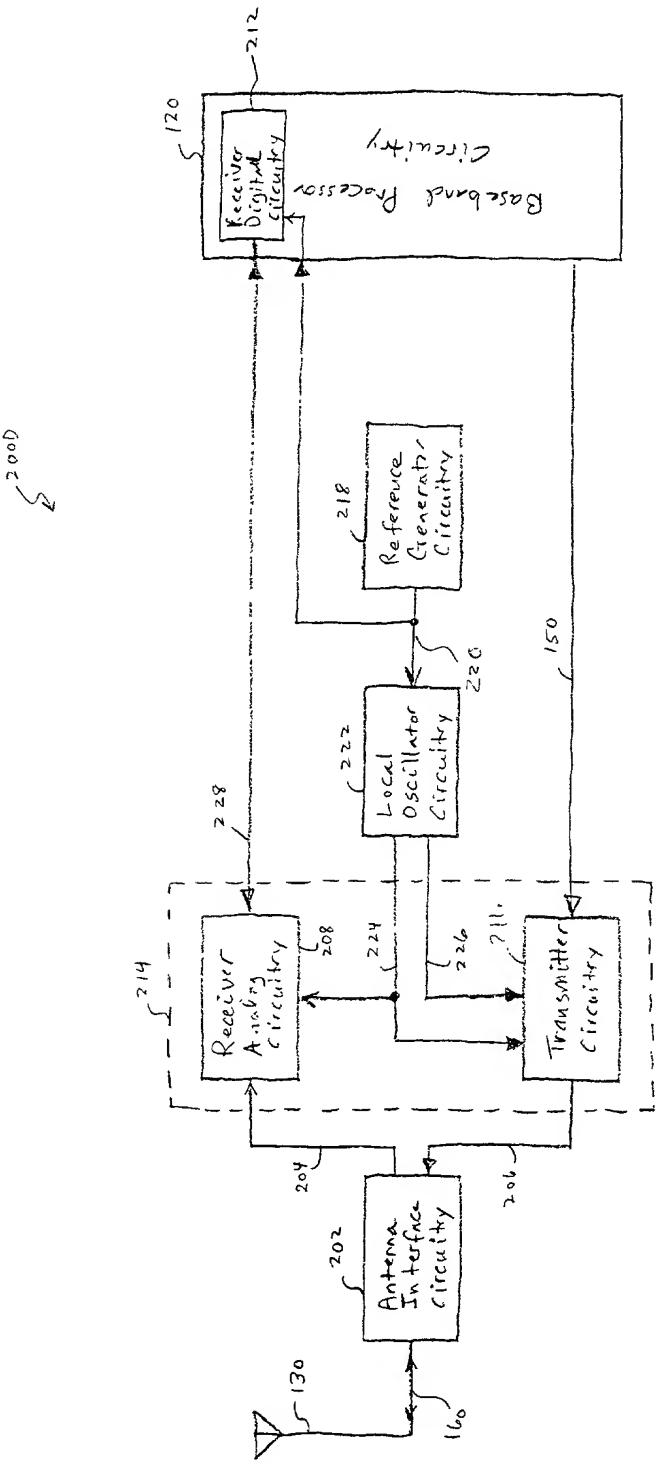


FIG. 2 D

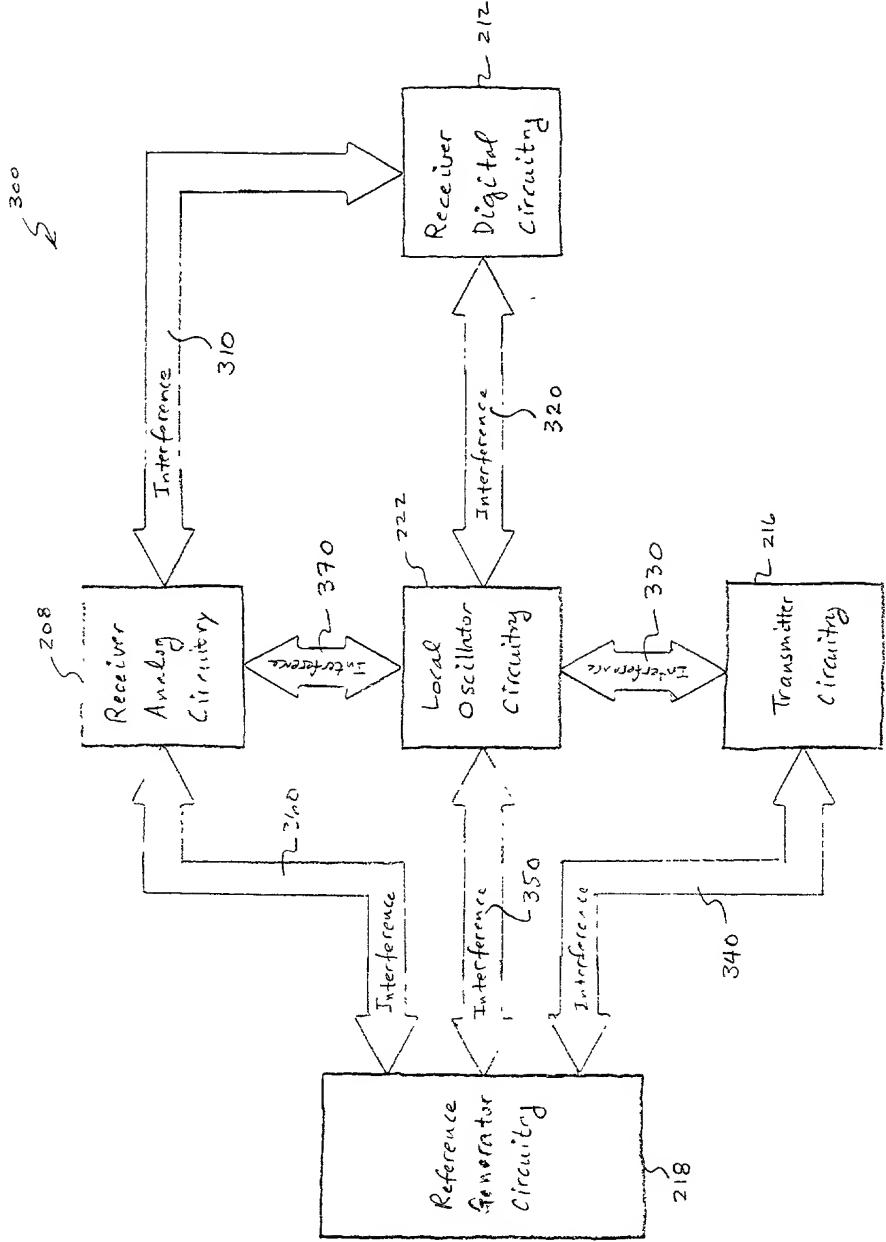
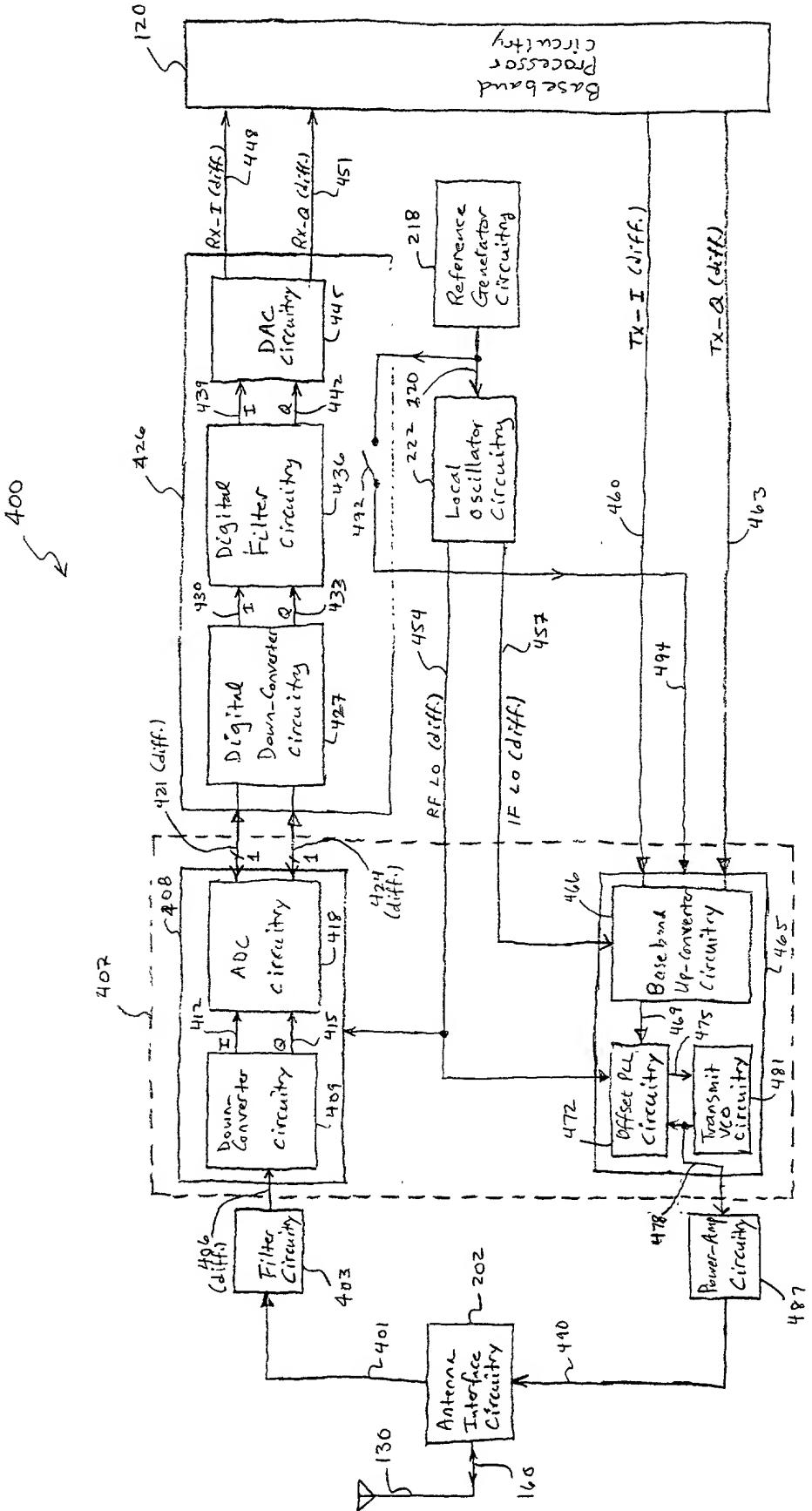


FIG. 3



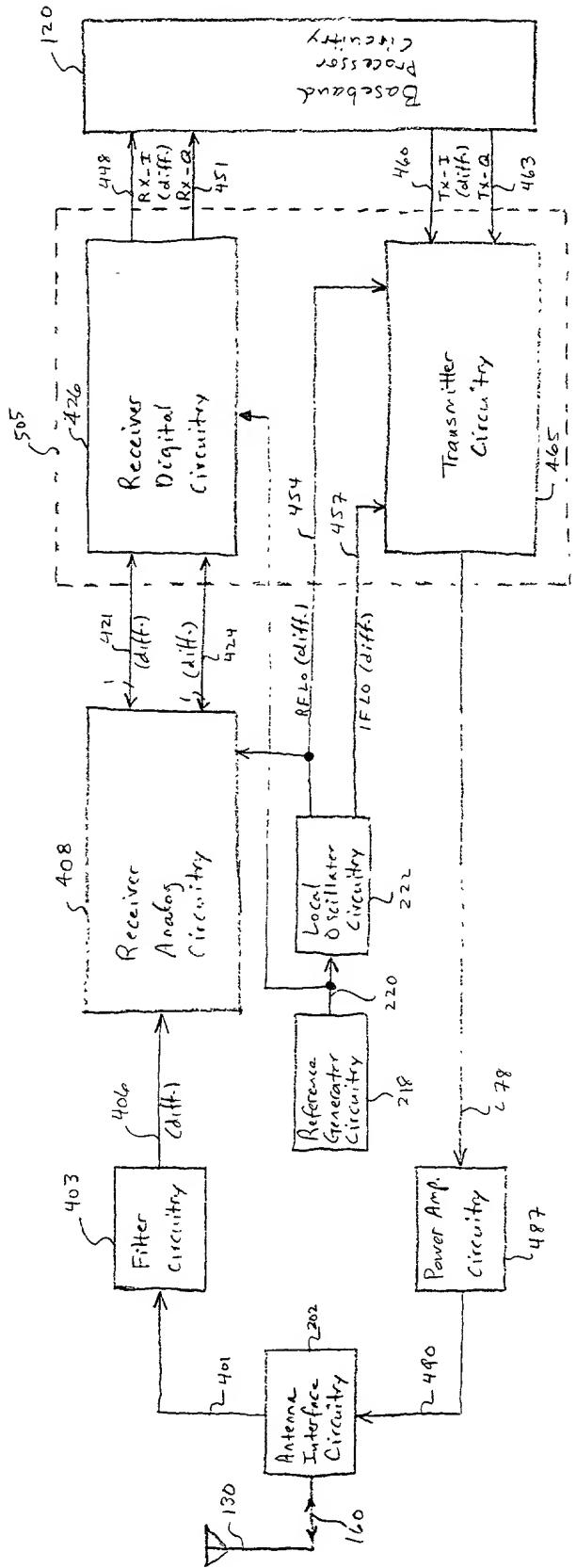


FIG. 5

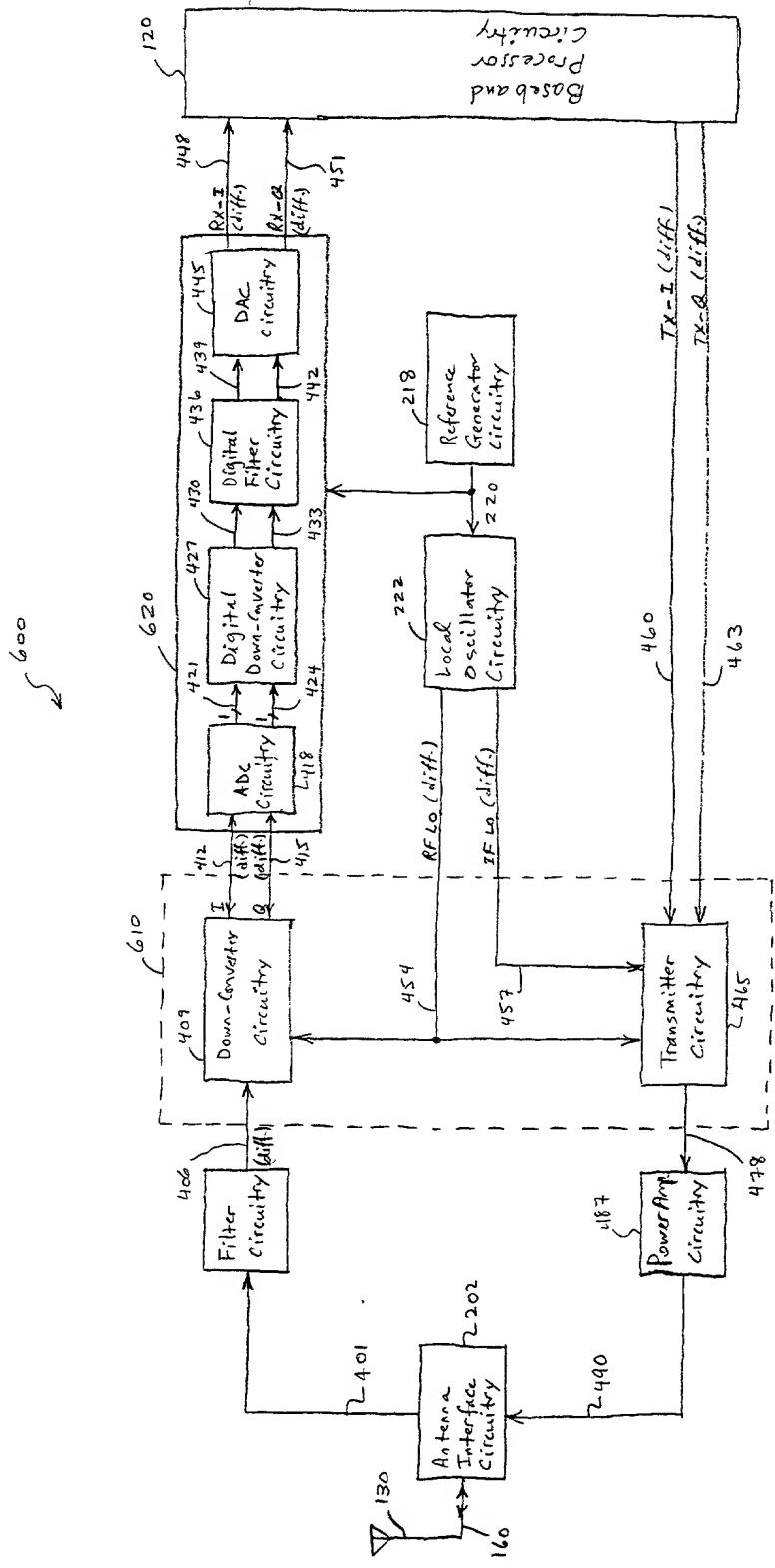


FIG. 6

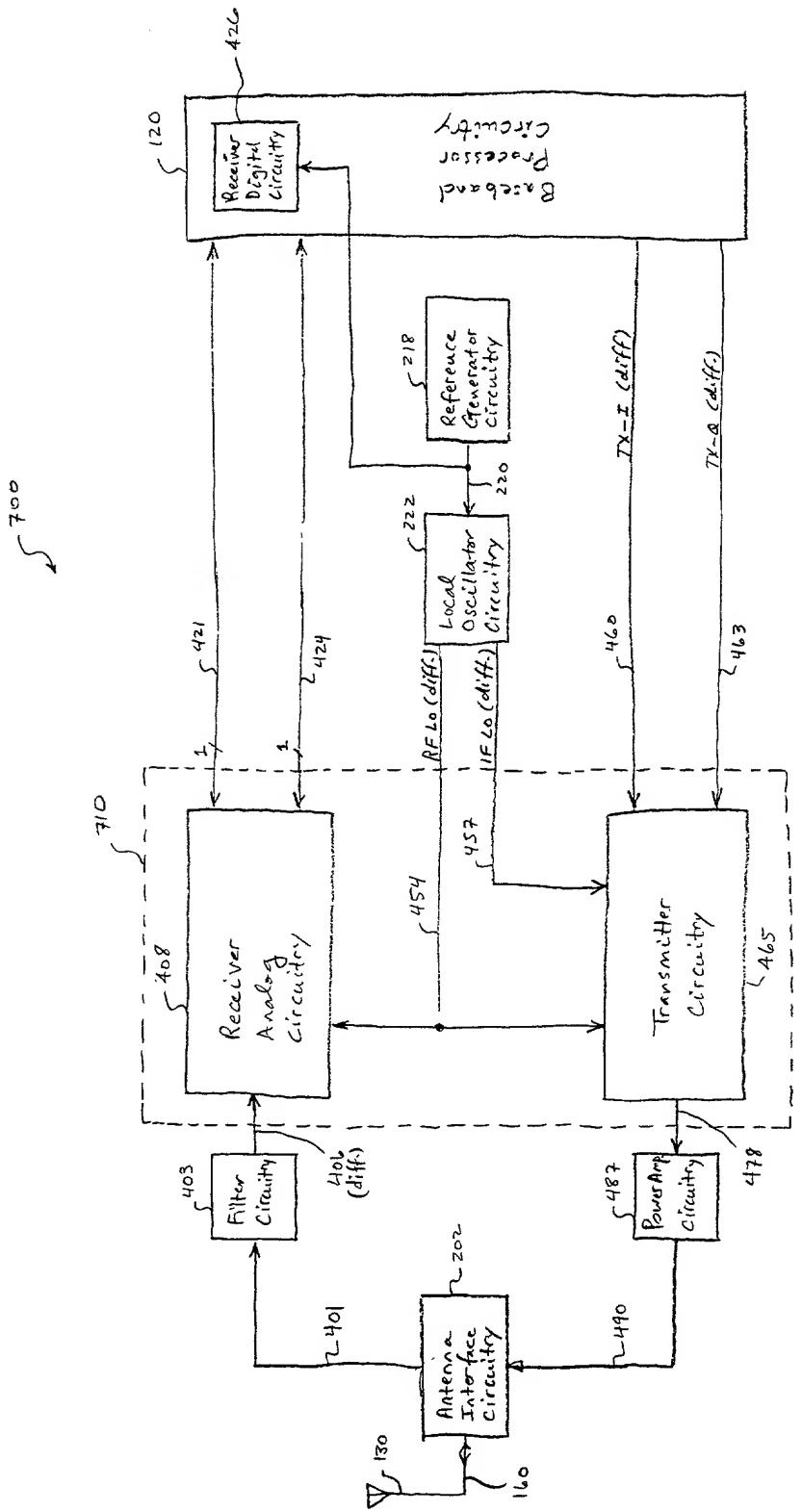


FIG. 7

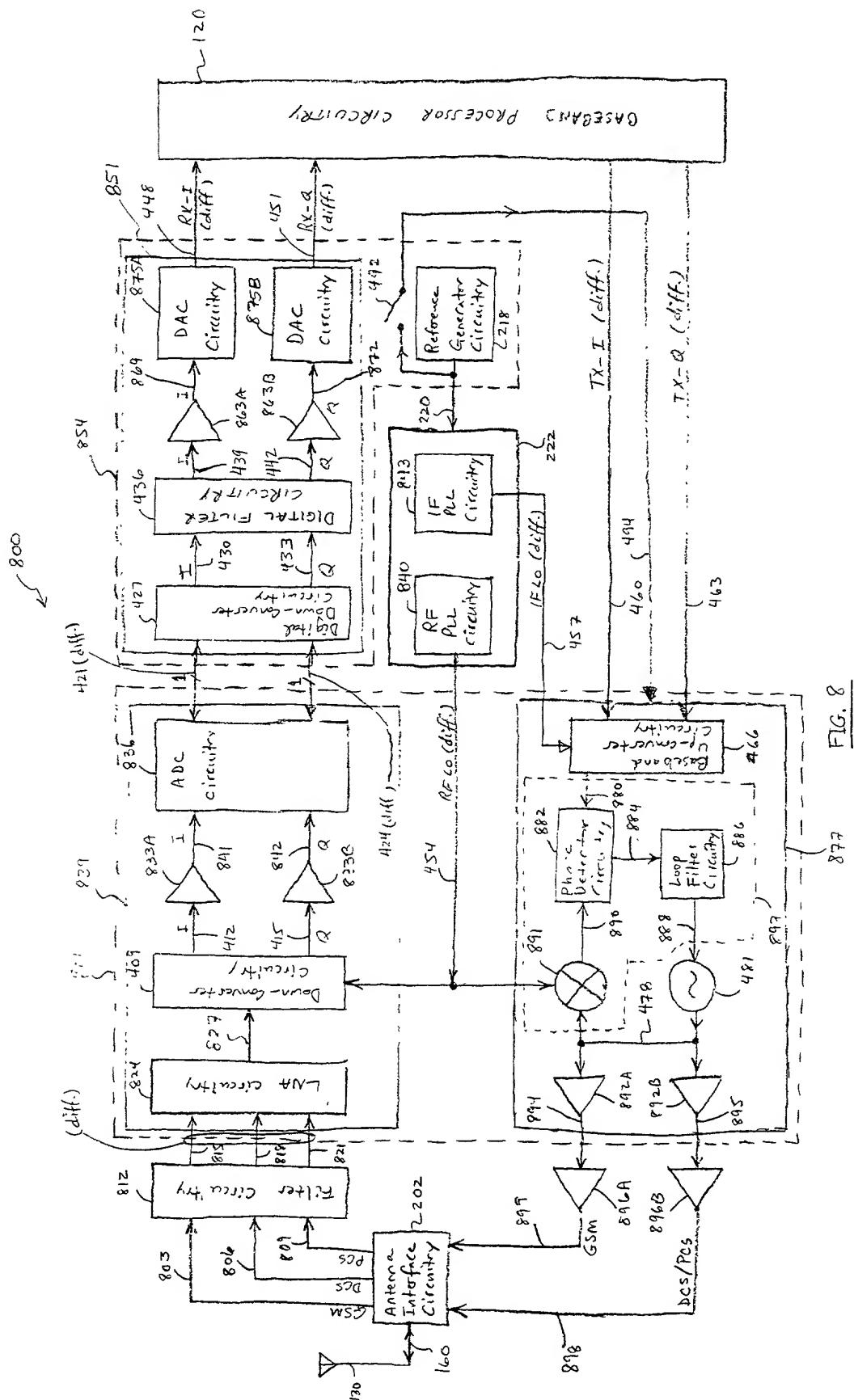


FIG. 8

DATA FLOW IN TTE BOARD

900A

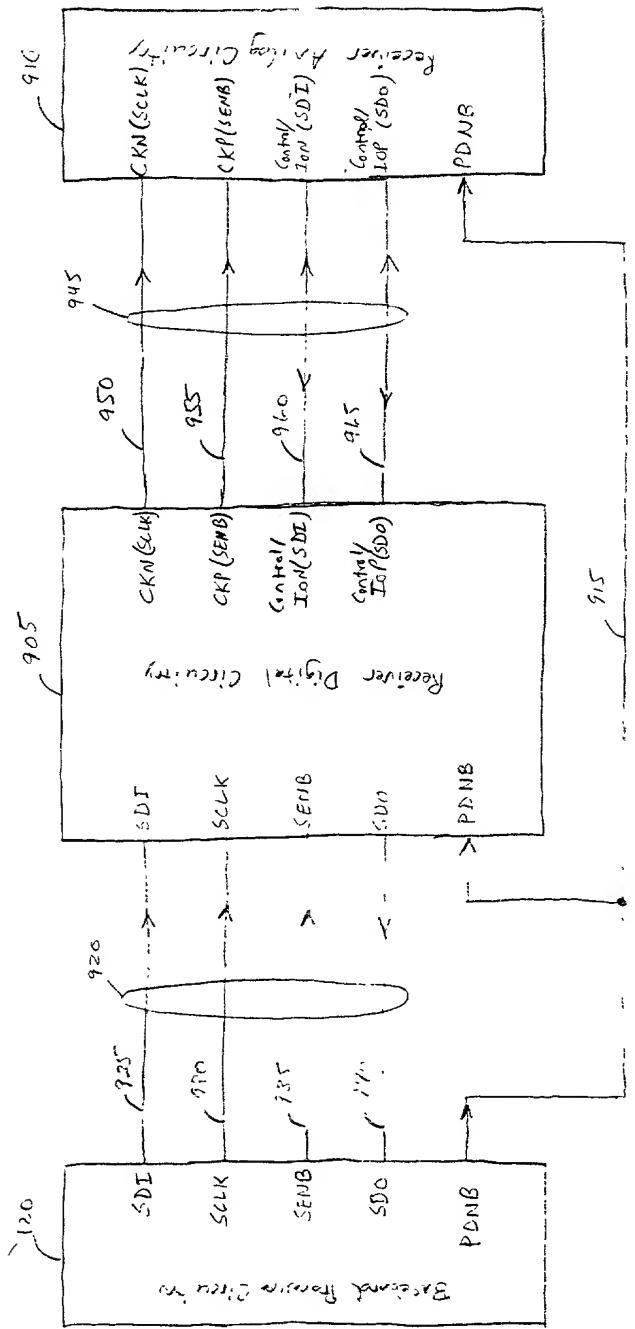


FIG. 9A

Digital Processor

$\swarrow 900B$

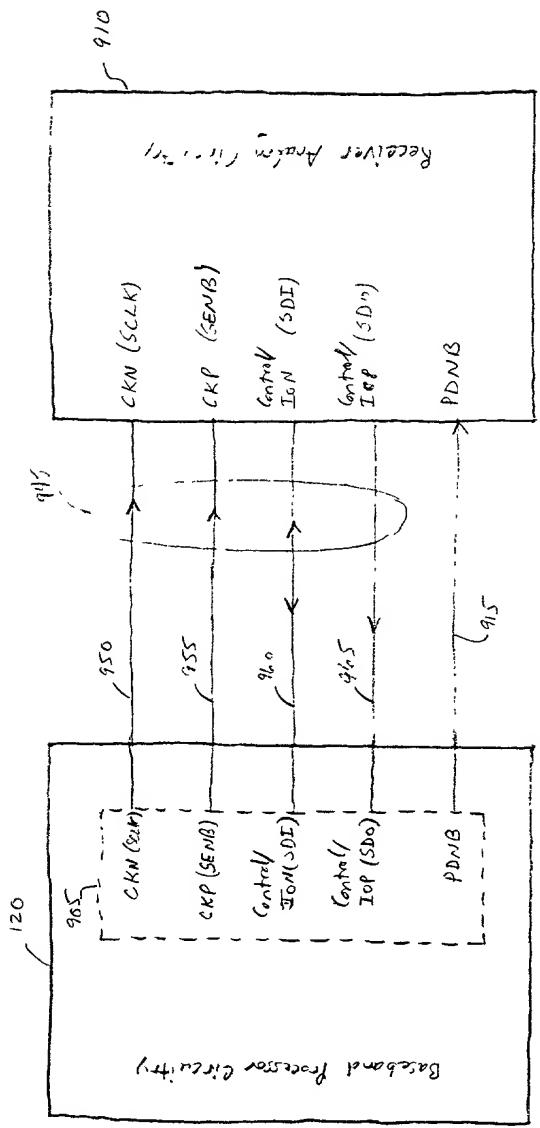


FIG. 9B

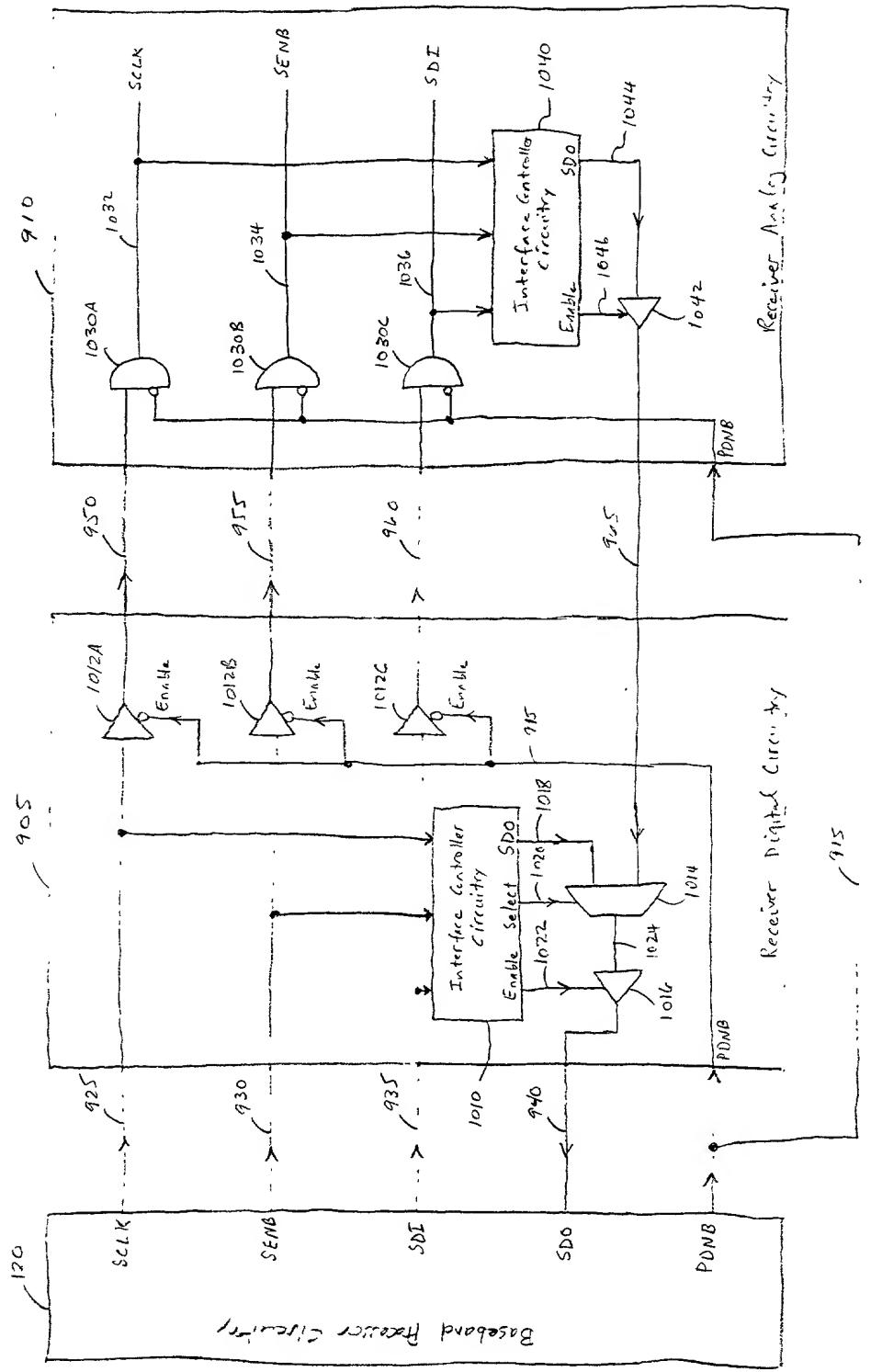
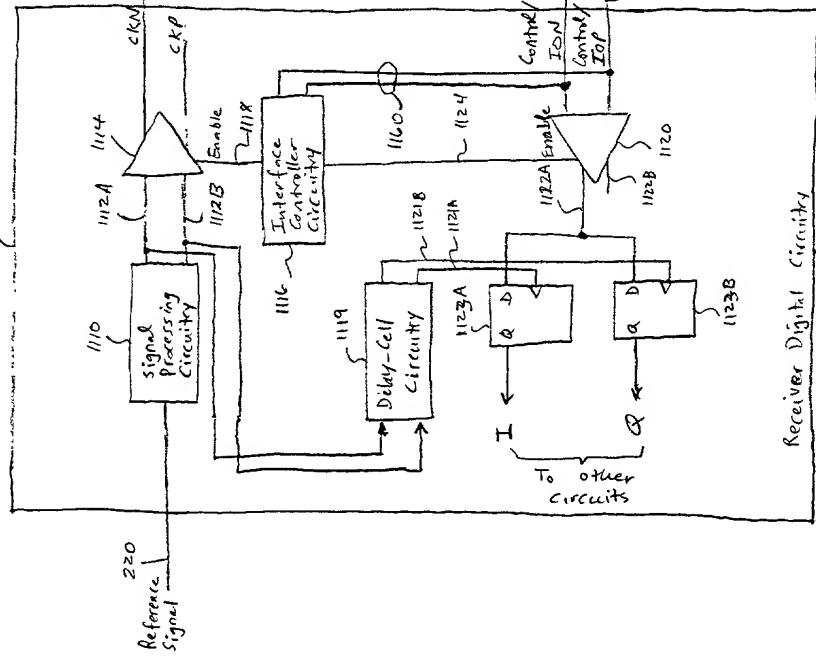


FIG. 10

220 Reference signal

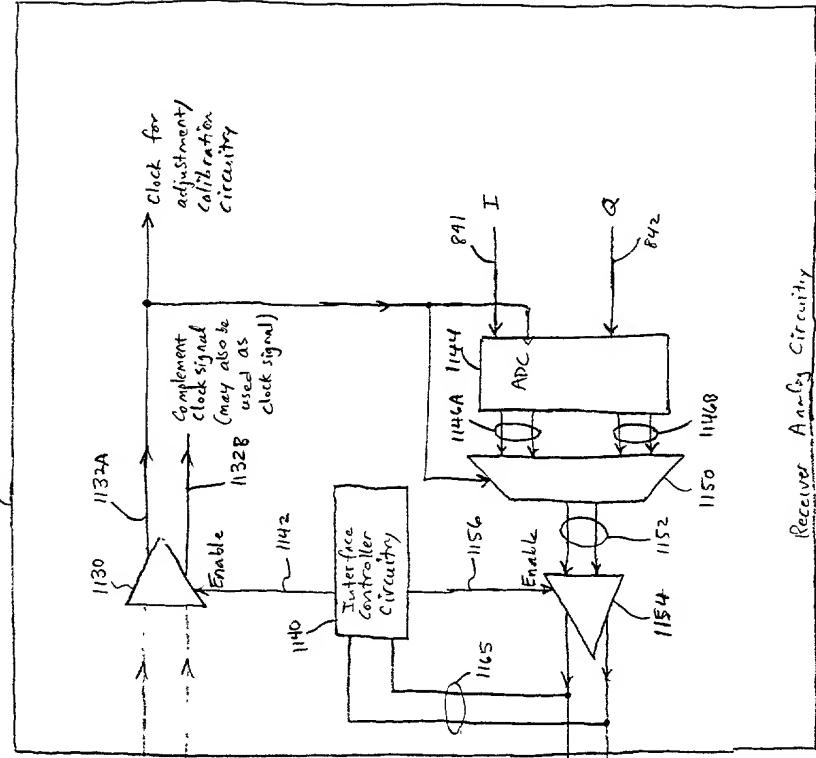
1100A

905



Receiver Digital Circuity

910



Receiver Analog Circuity

FIG. 11A

FIGURE 11B

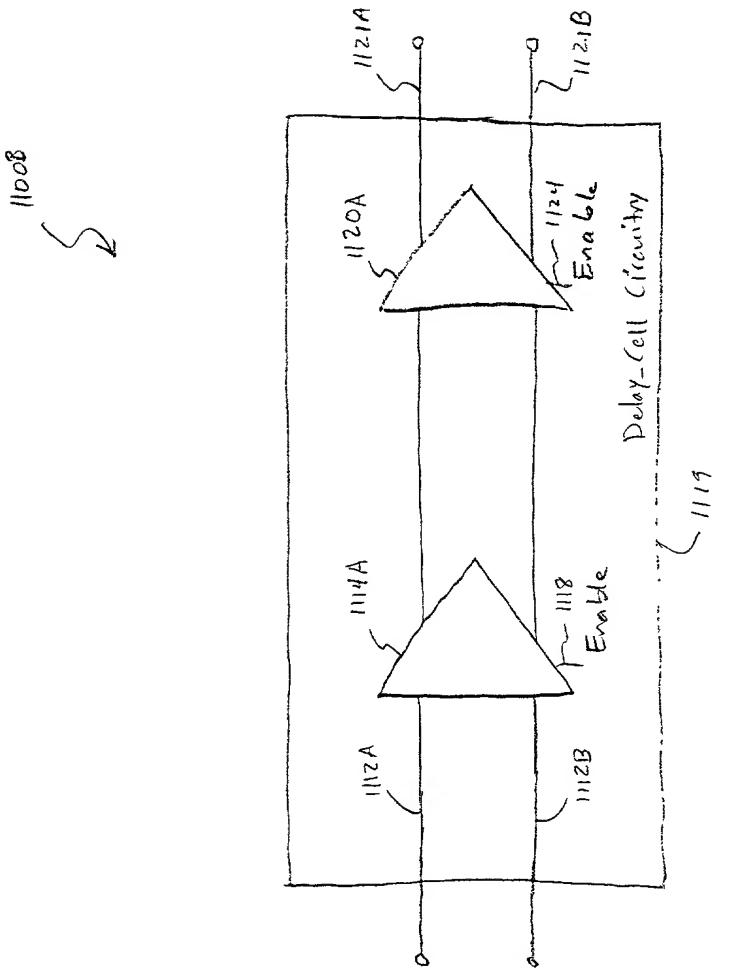
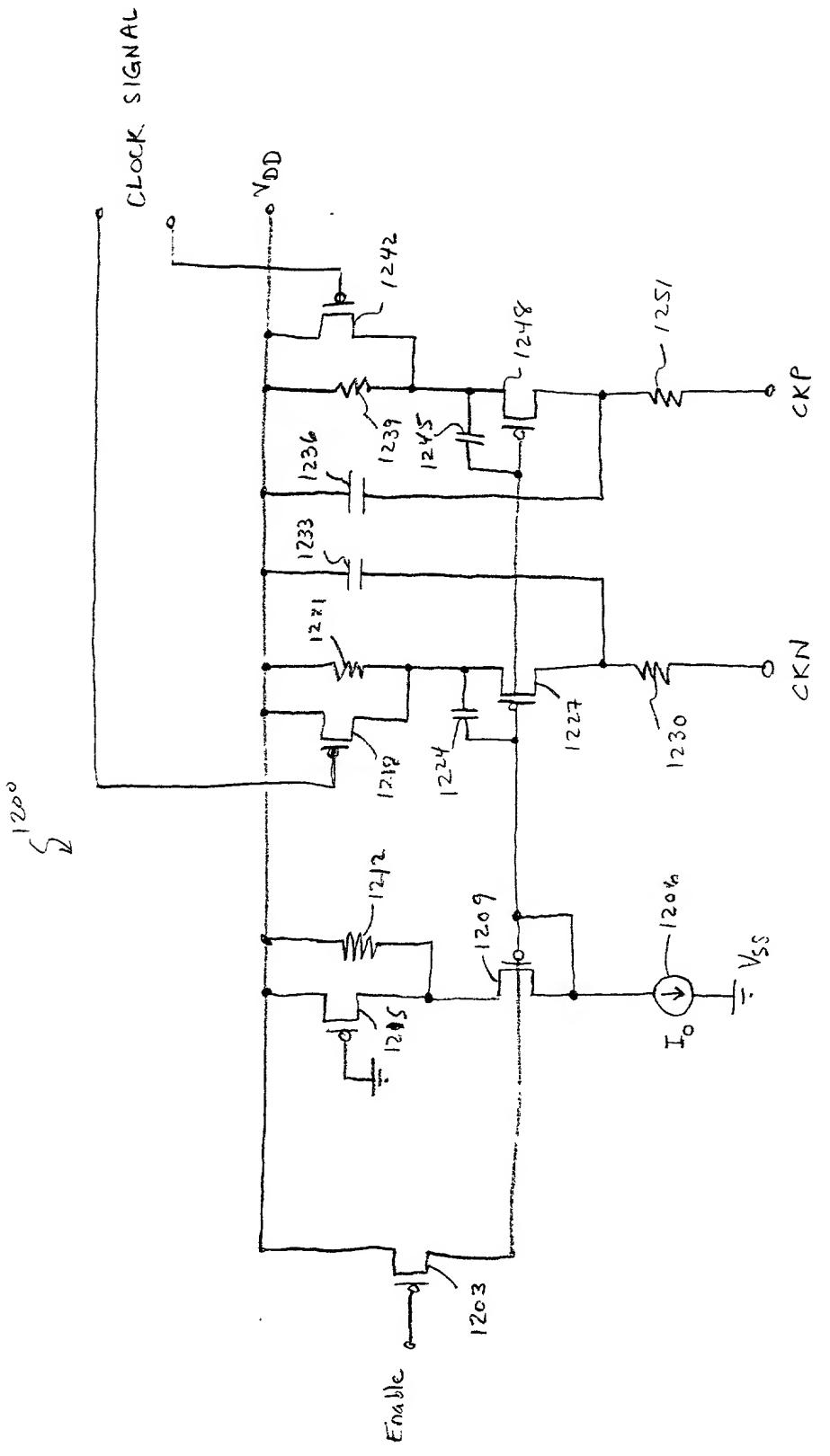
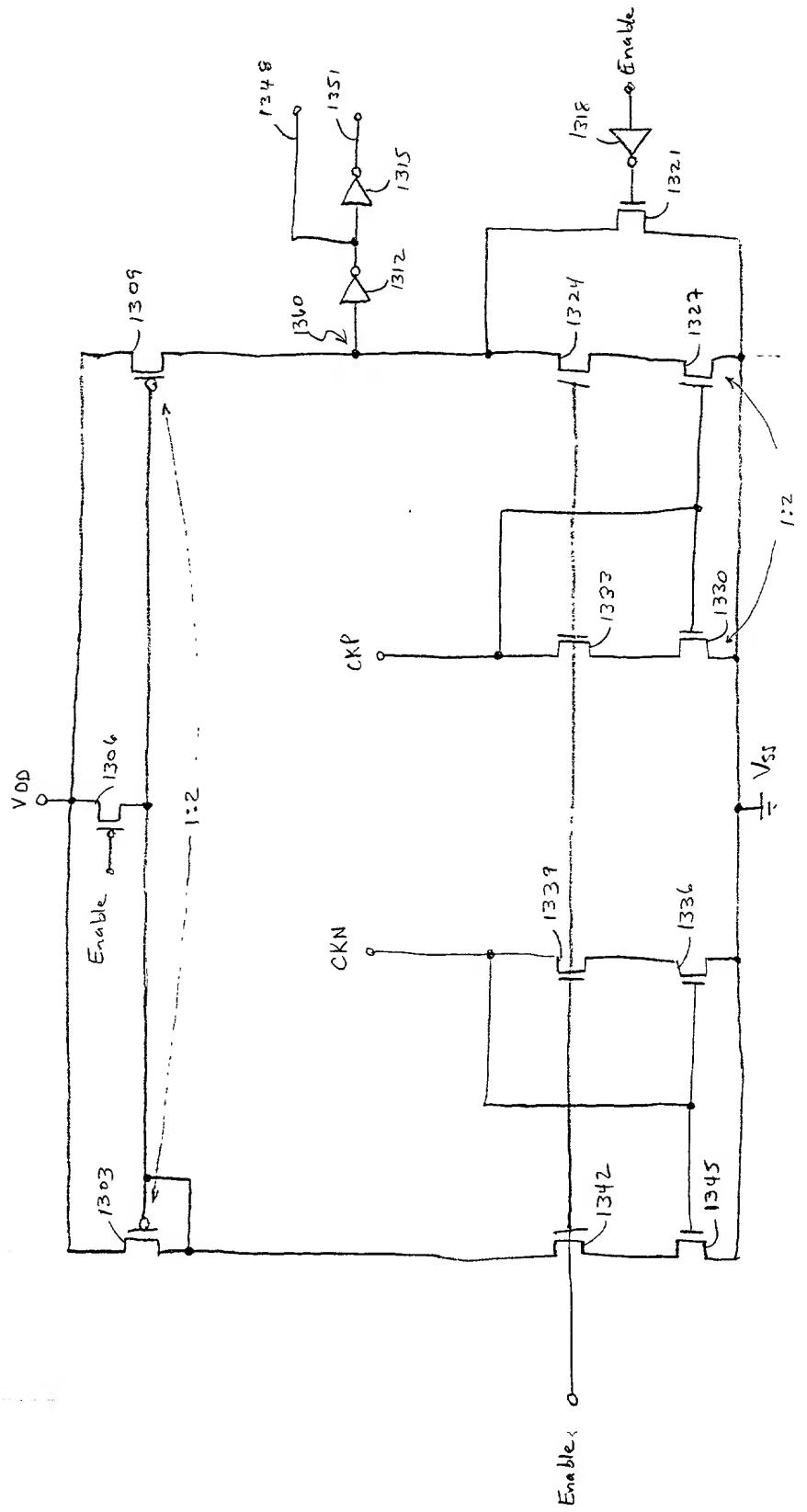
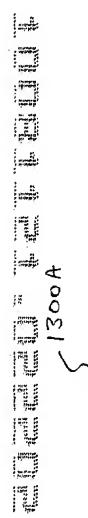


Fig. 11B



F-16.12



F16. 13A

電子技術圖 = 電子圖

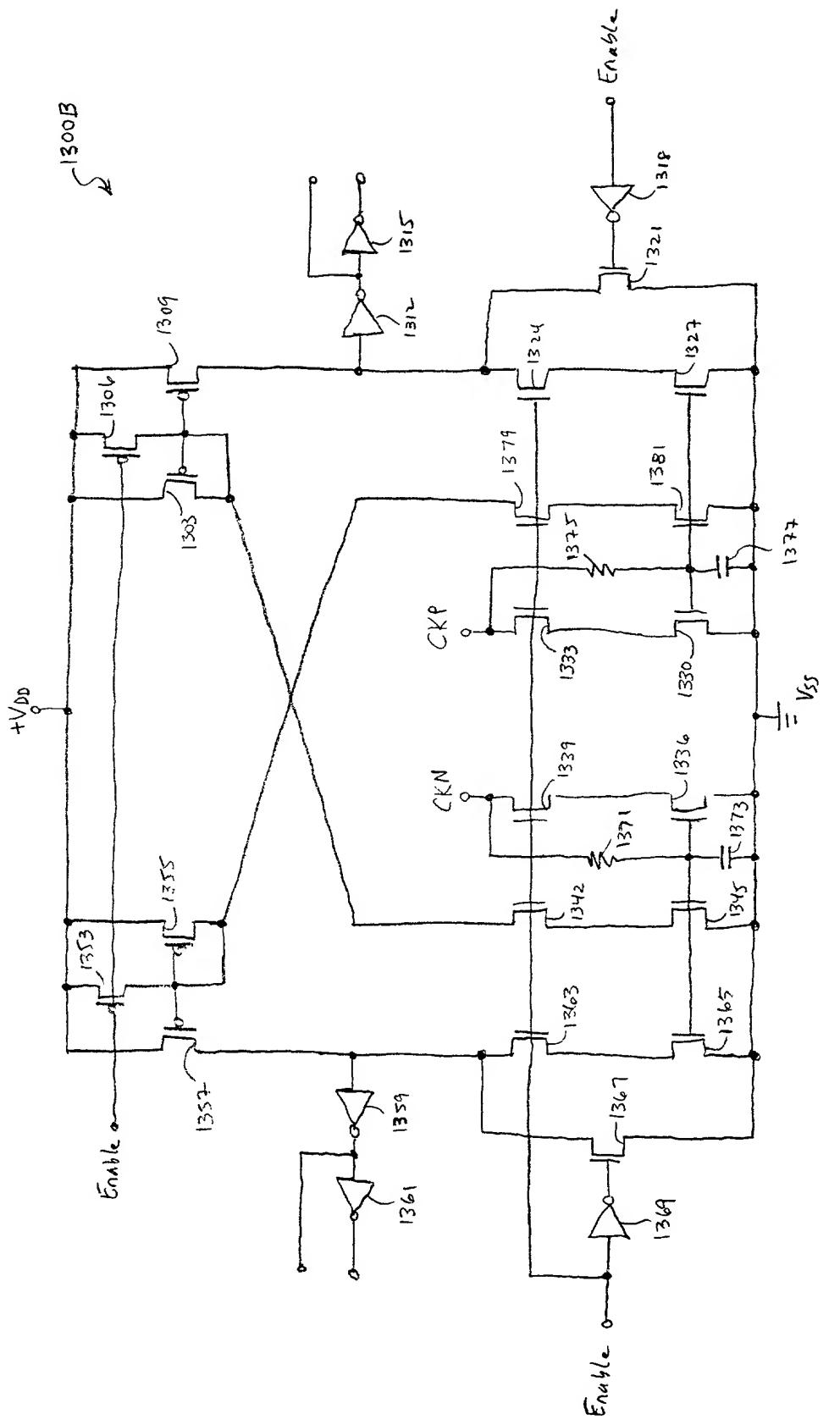


FIG. 13B

1400

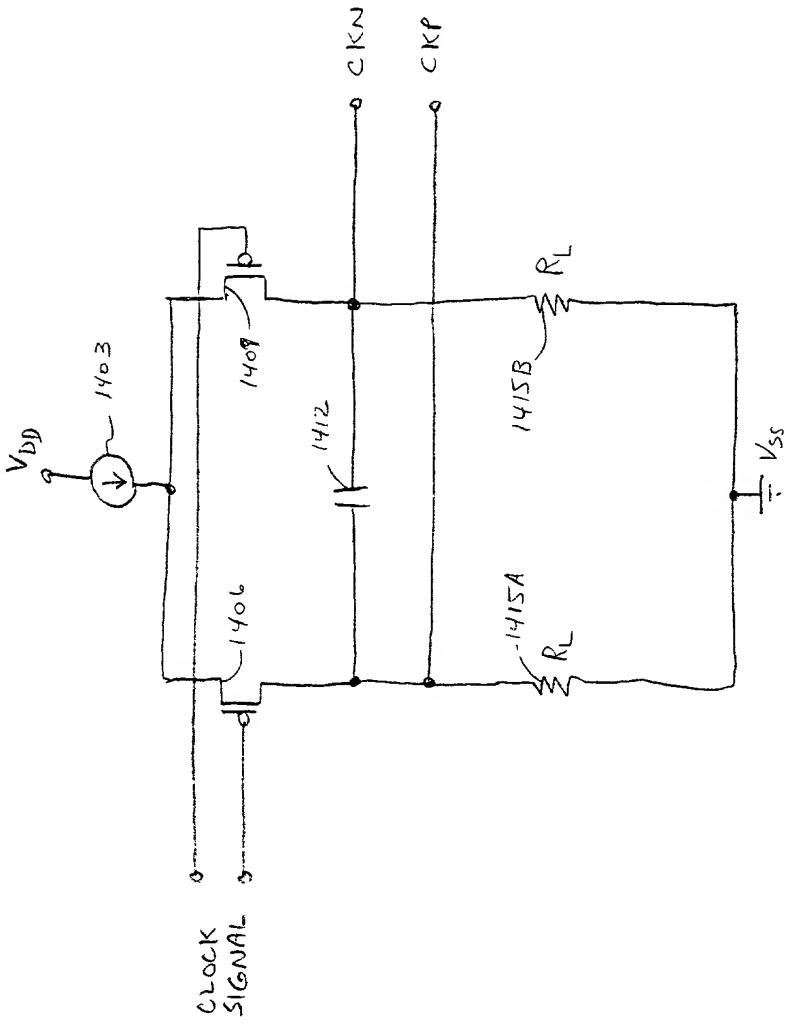


FIG. 14

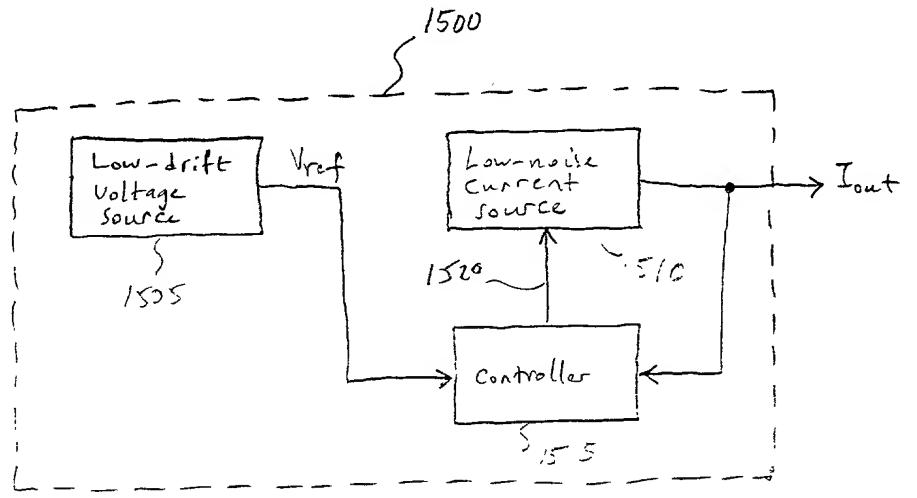


FIG. 15

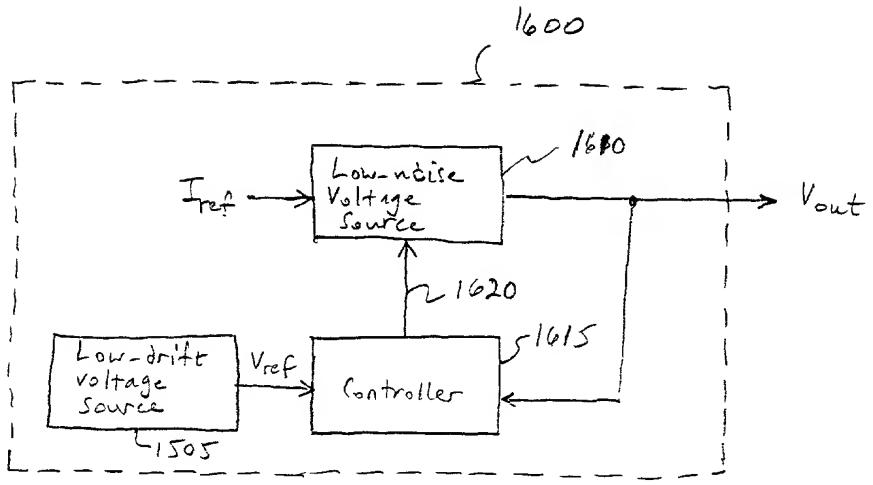


FIG. 16

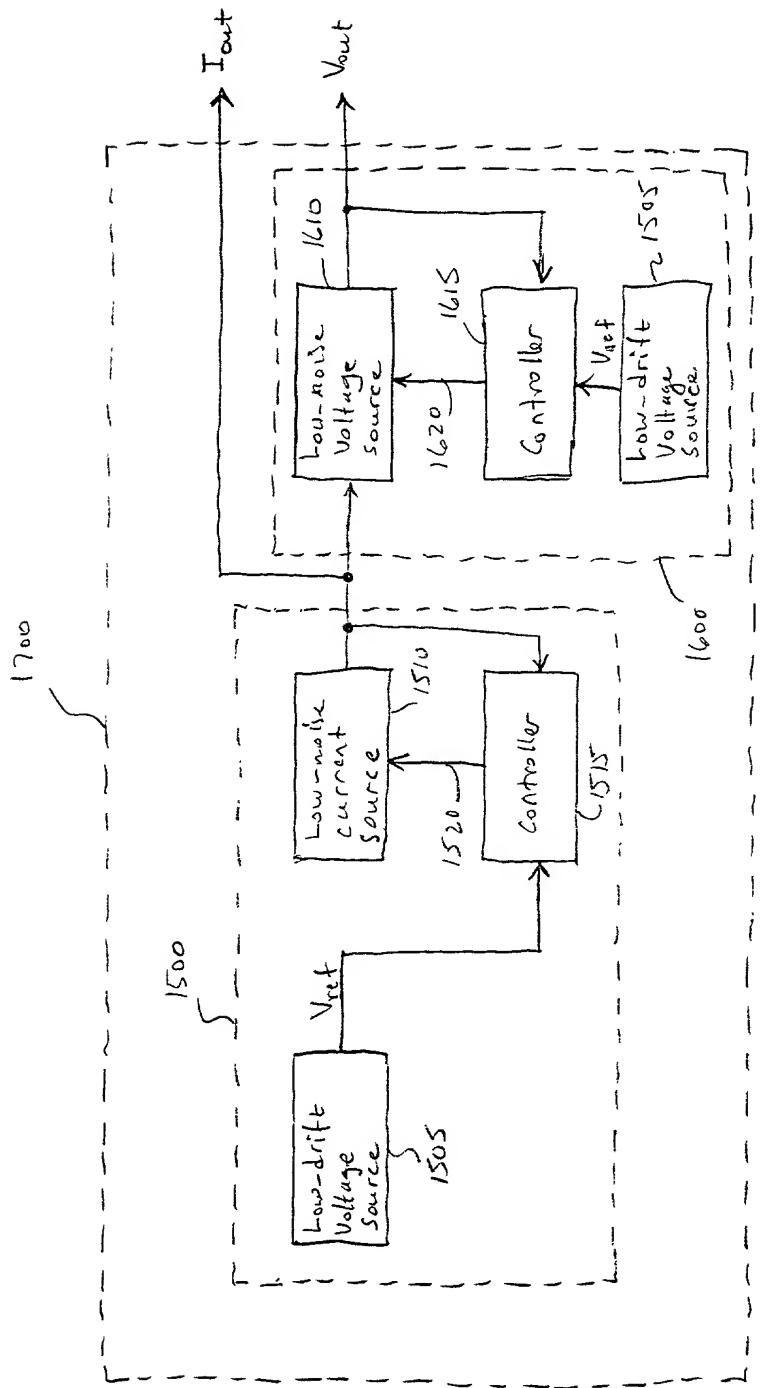


FIG. 17

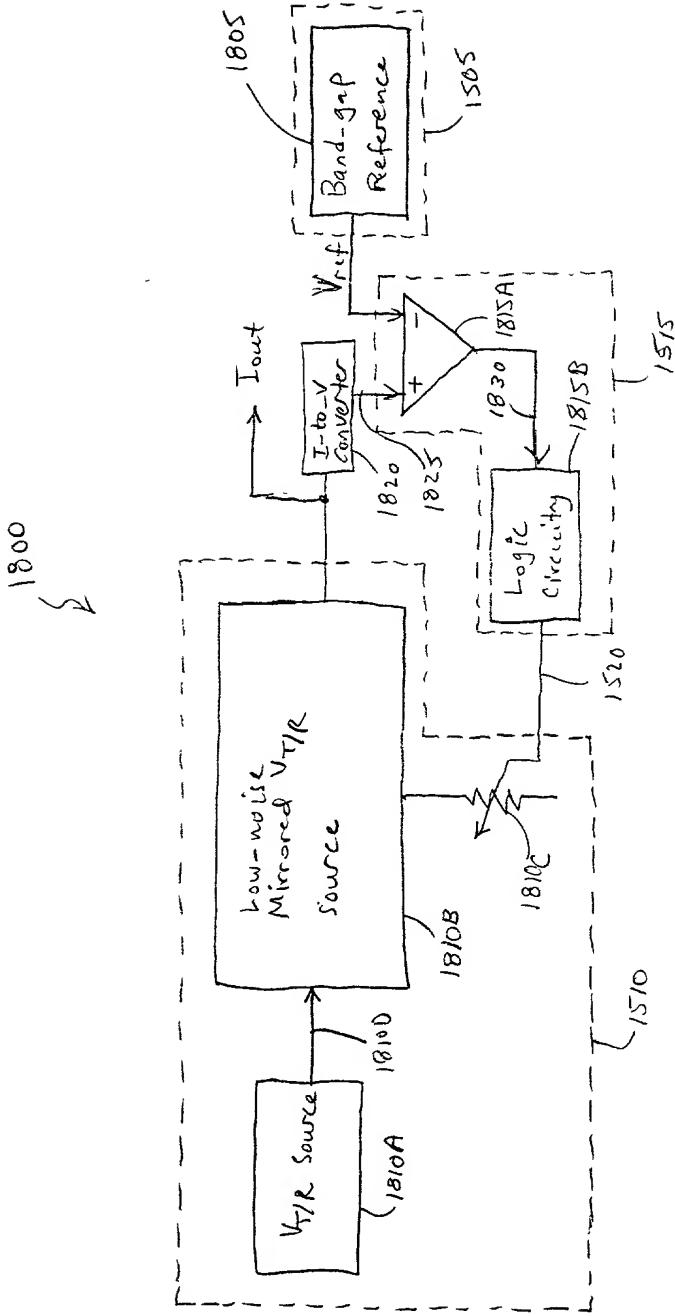


FIG. 18

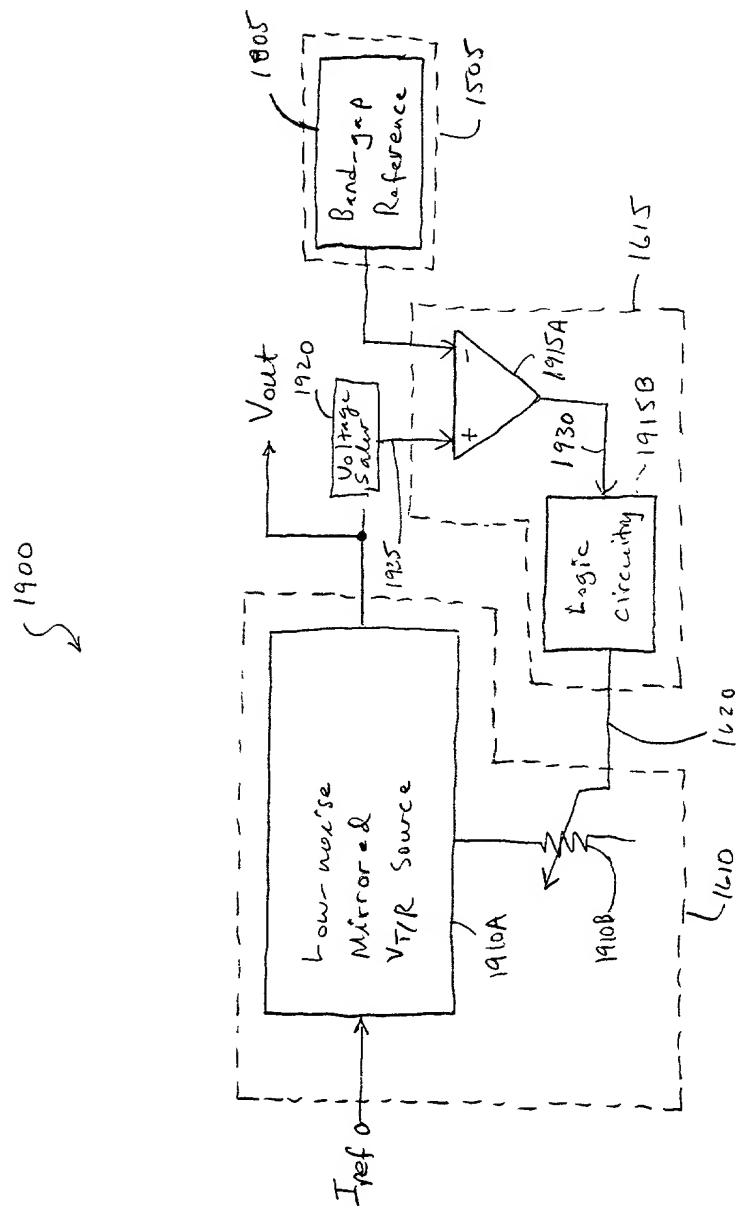


FIG. 19

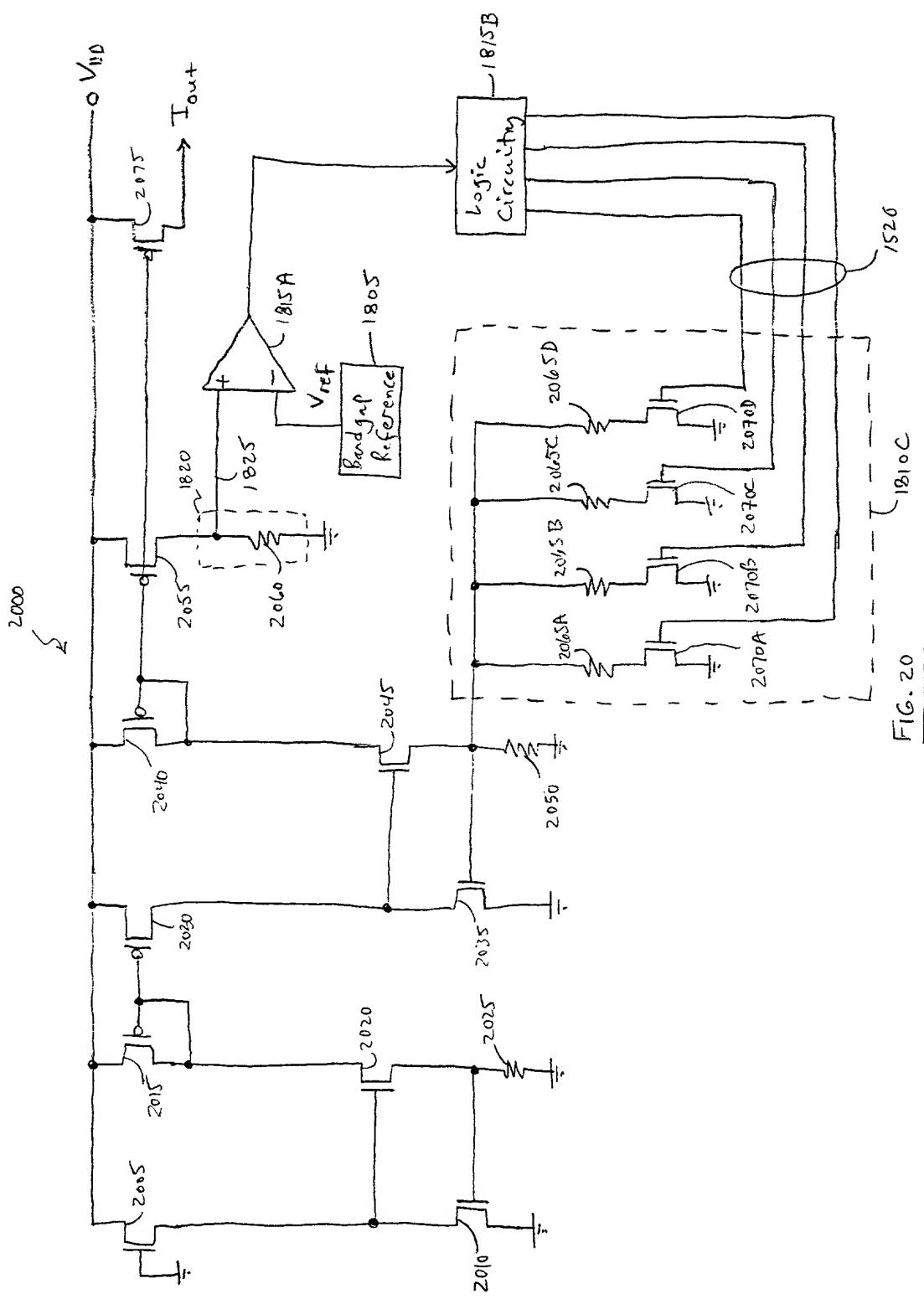


FIG. 20

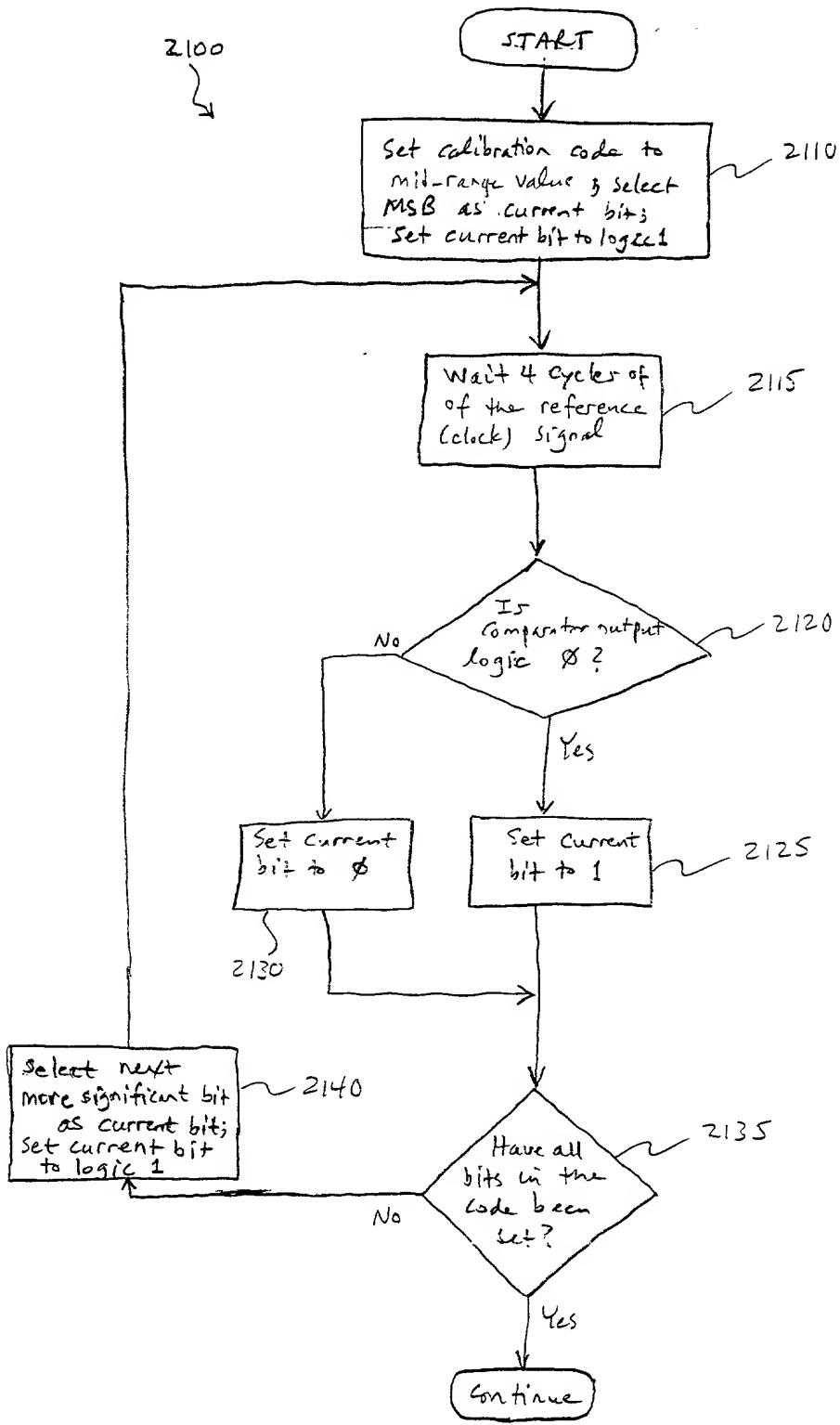


FIG. 21

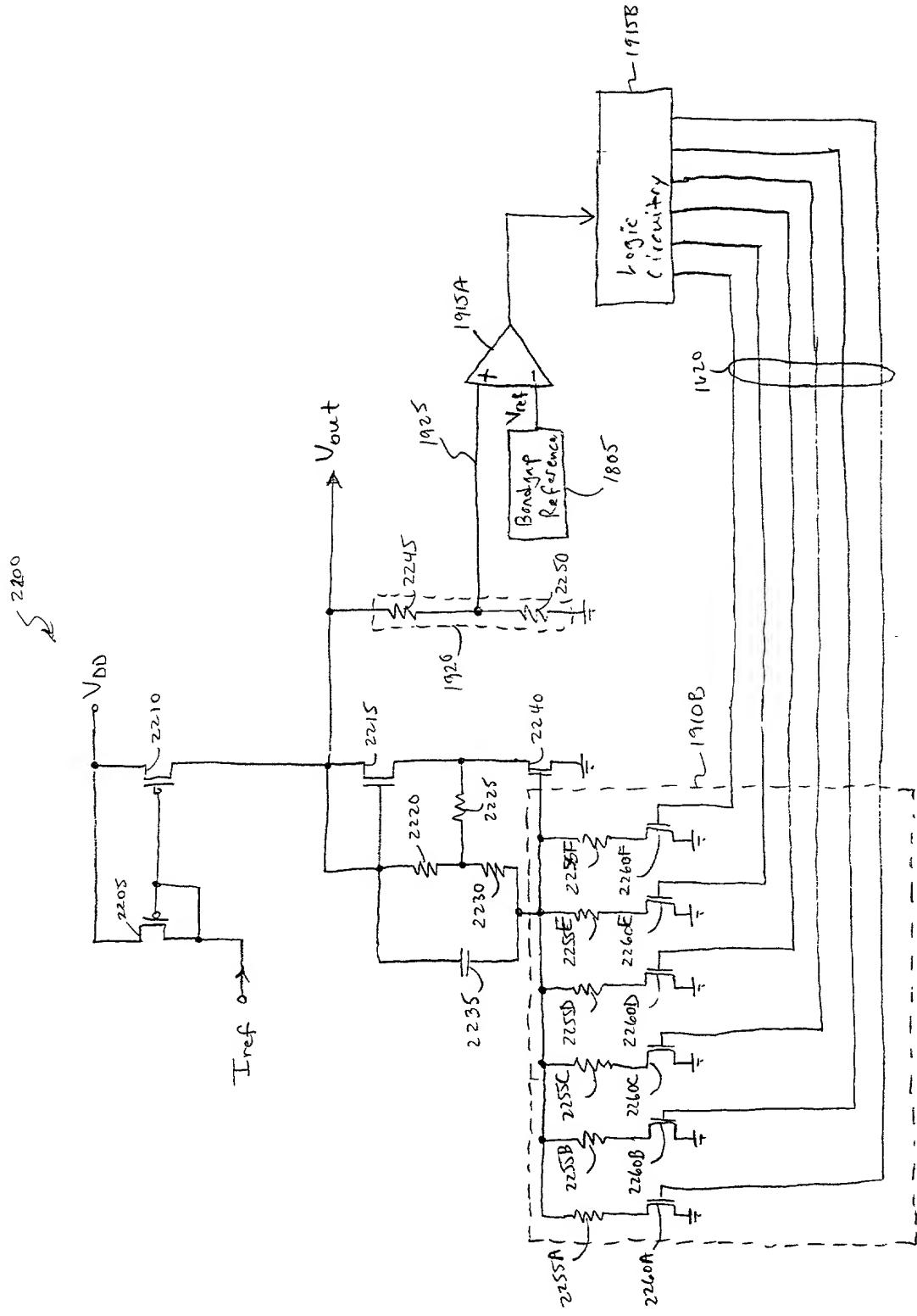


FIG. 22

2300
↓

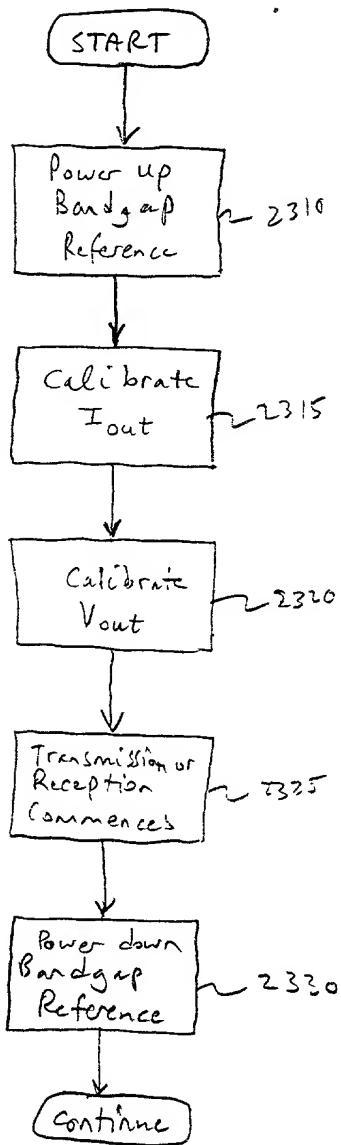


FIG. 23

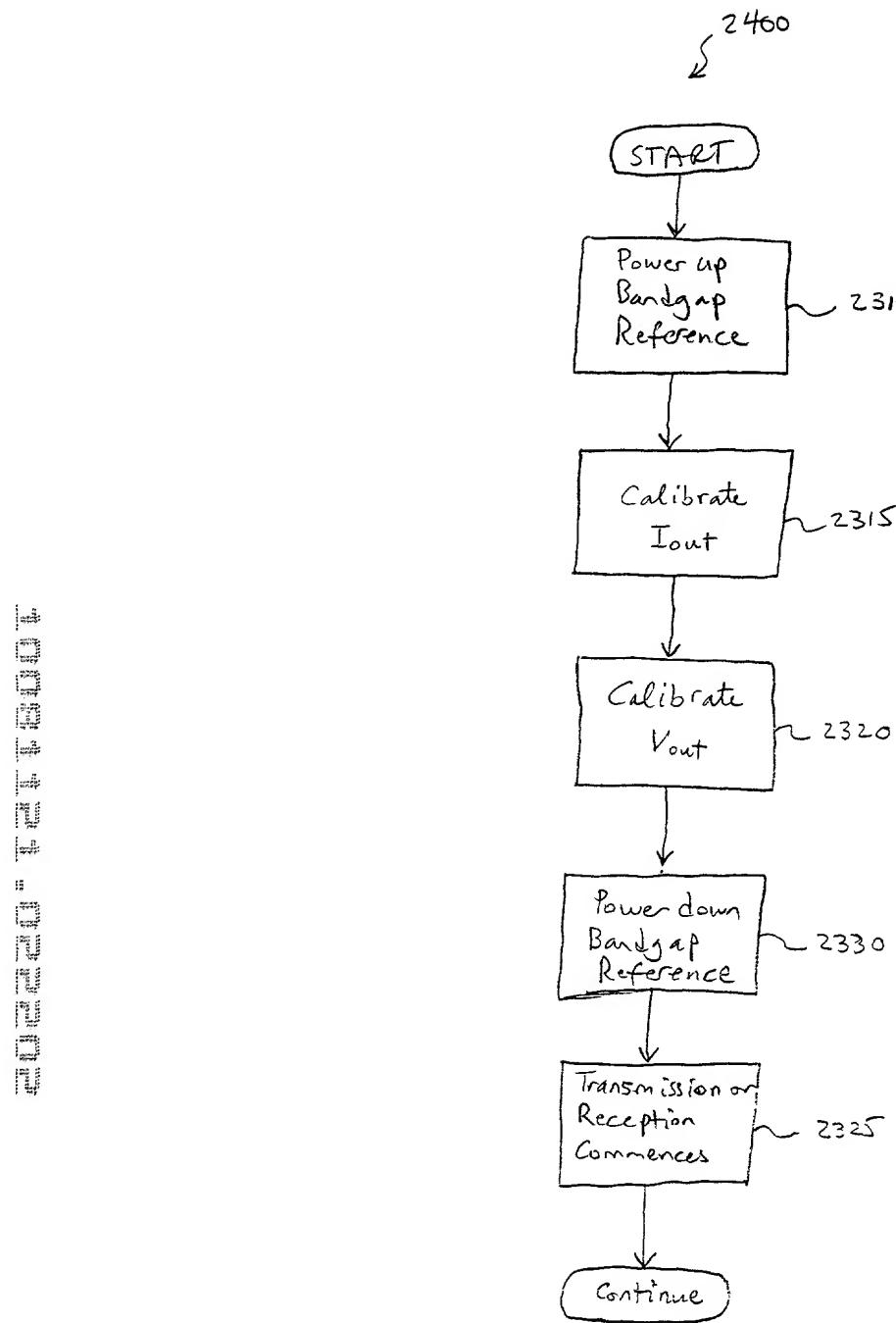


FIG. 24